

FIG. 1

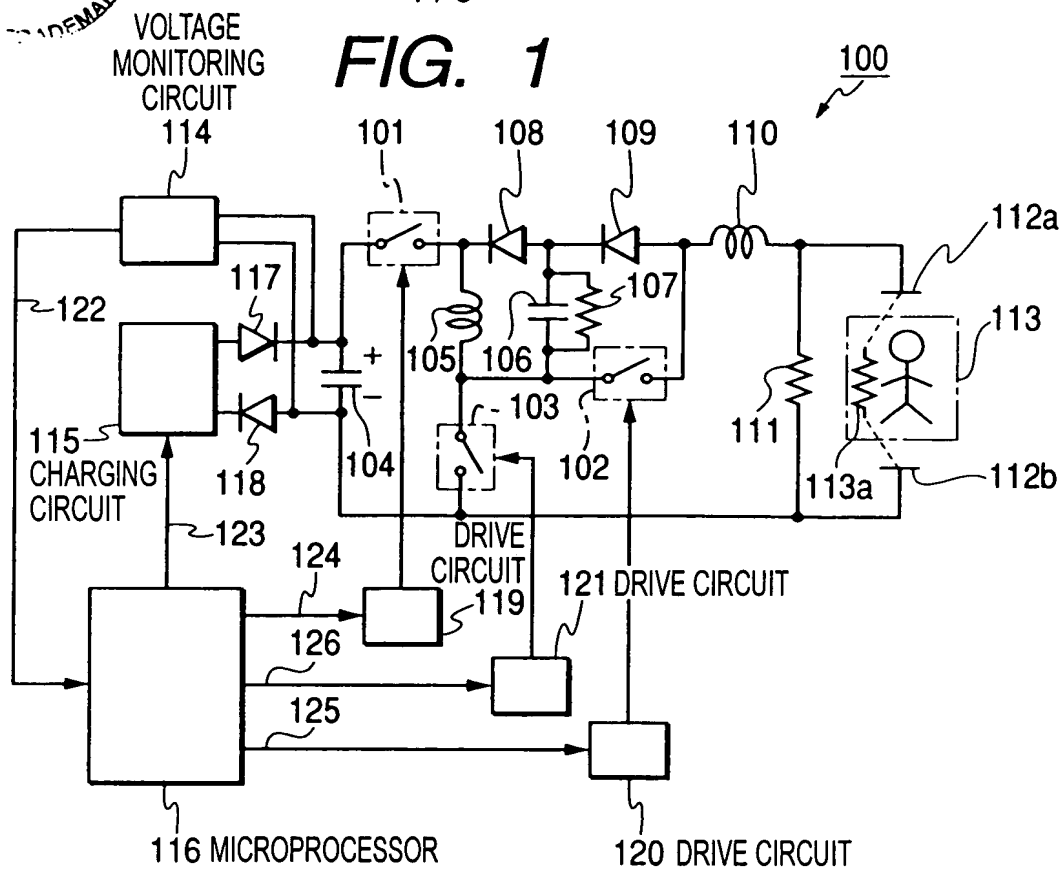
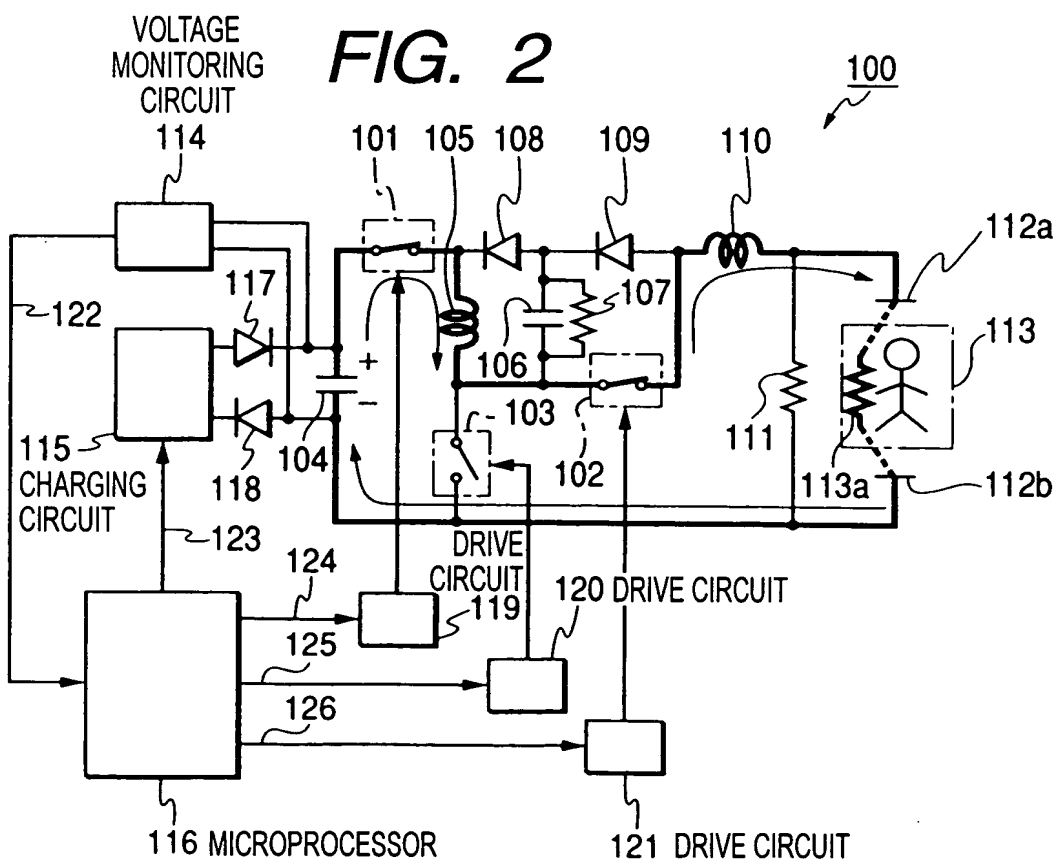


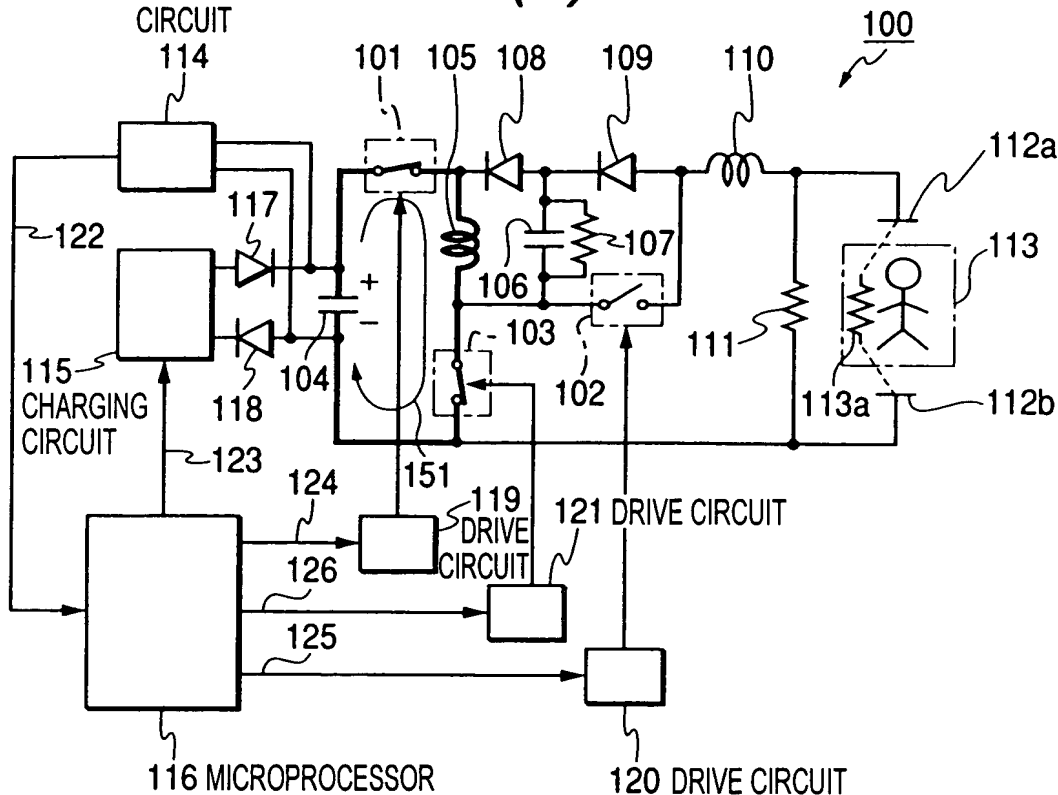
FIG. 2





VOLTAGE
MONITORING
CIRCUIT

FIG. 3(a)



VOLTAGE
MONITORING
CIRCUIT

FIG. 3(b)

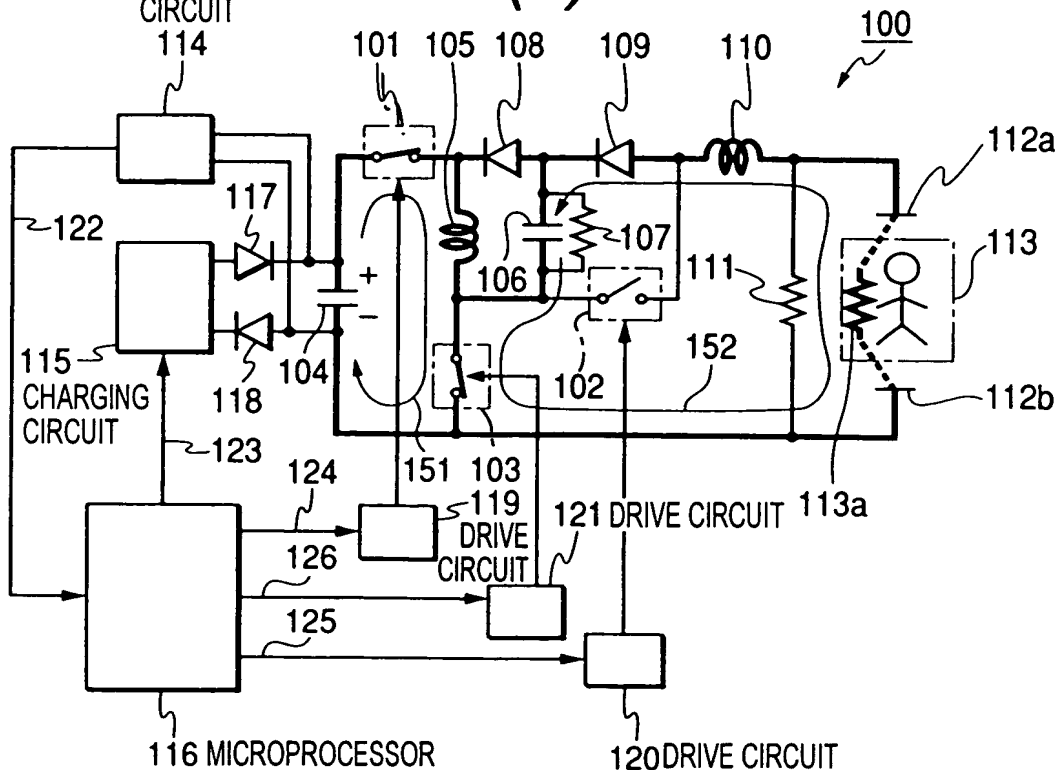




FIG. 4

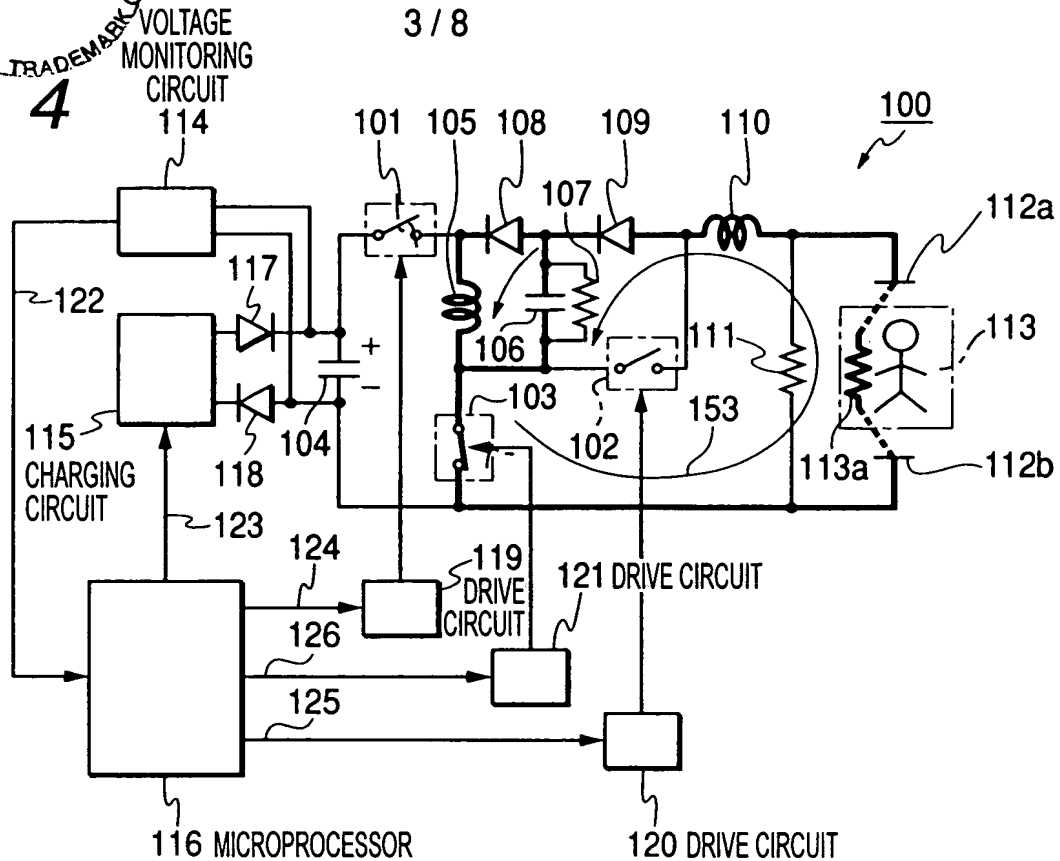


FIG. 5

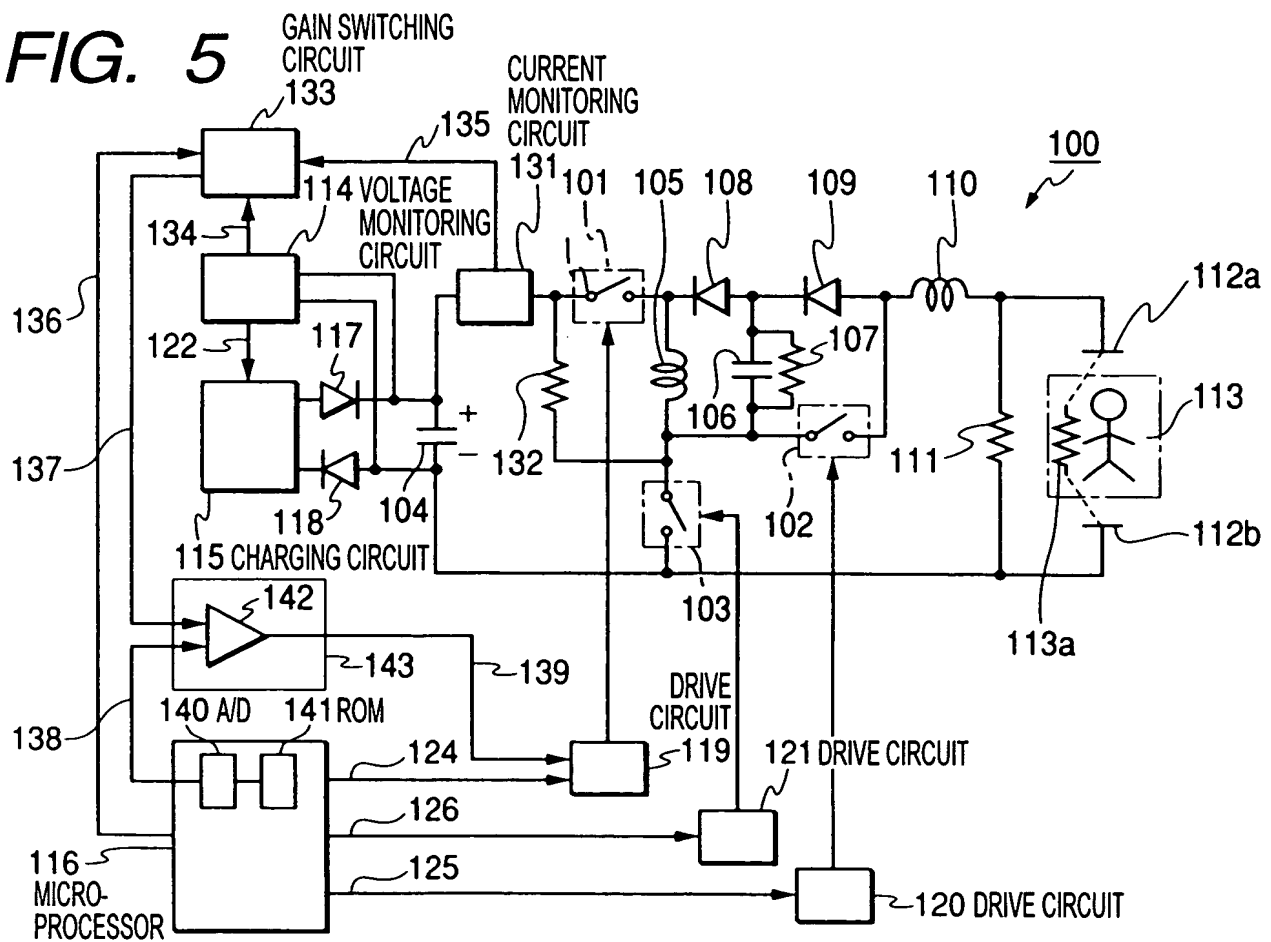




FIG. 6(a)

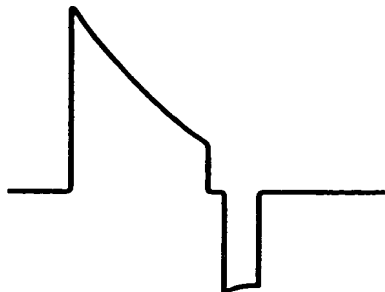


FIG. 6(b)

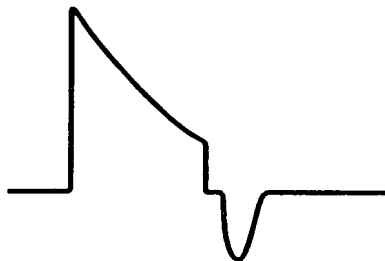


FIG. 6(c)

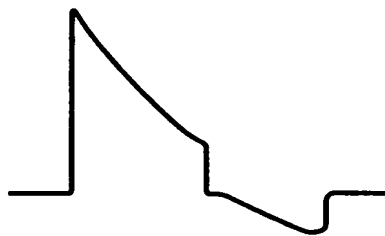


FIG. 6(d)

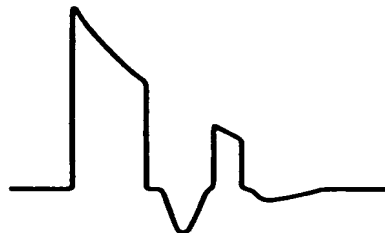




FIG. 7

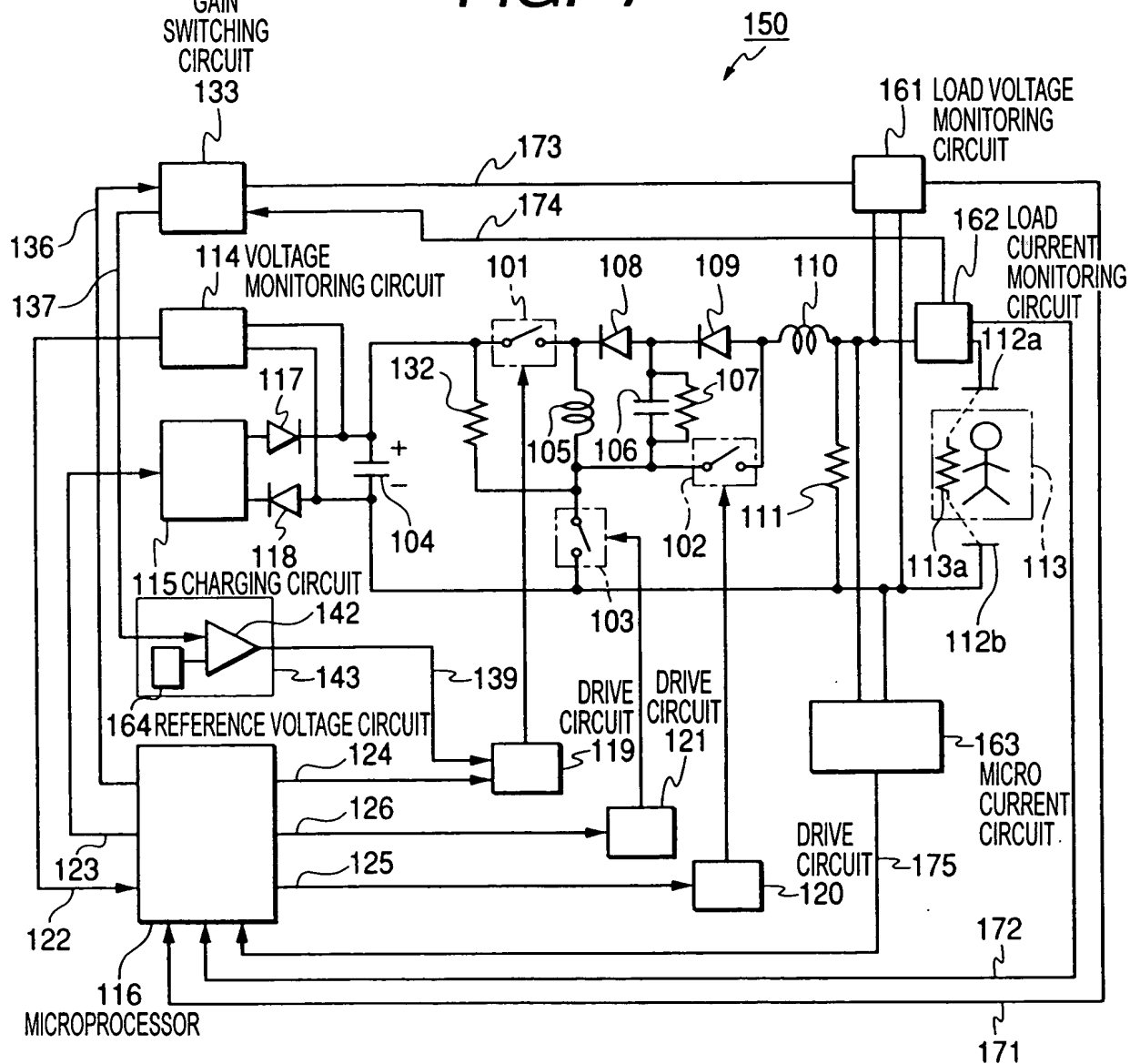




FIG. 8(a)

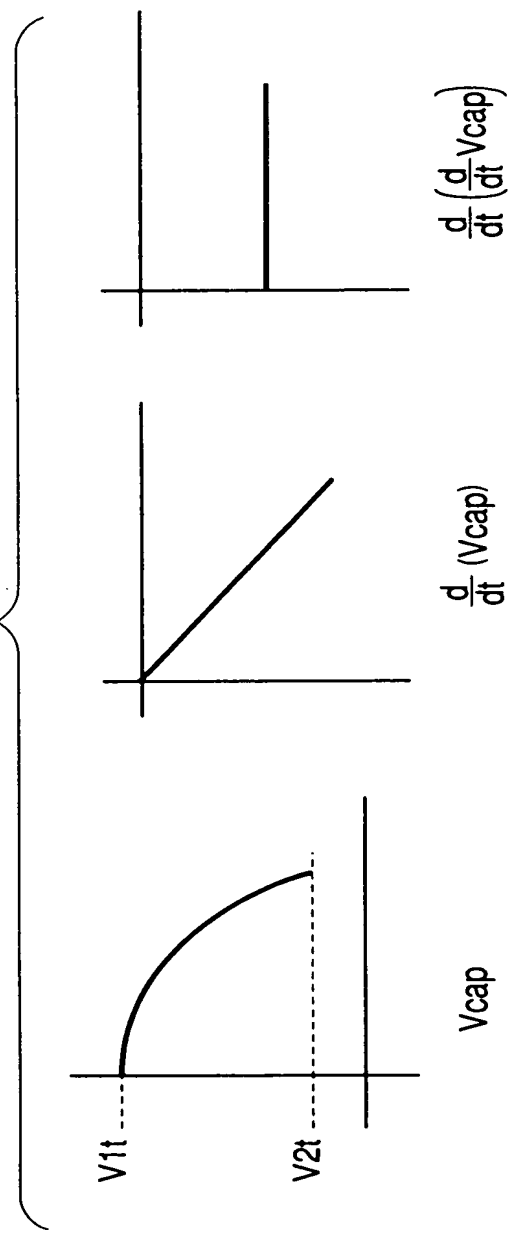


FIG. 8(b)

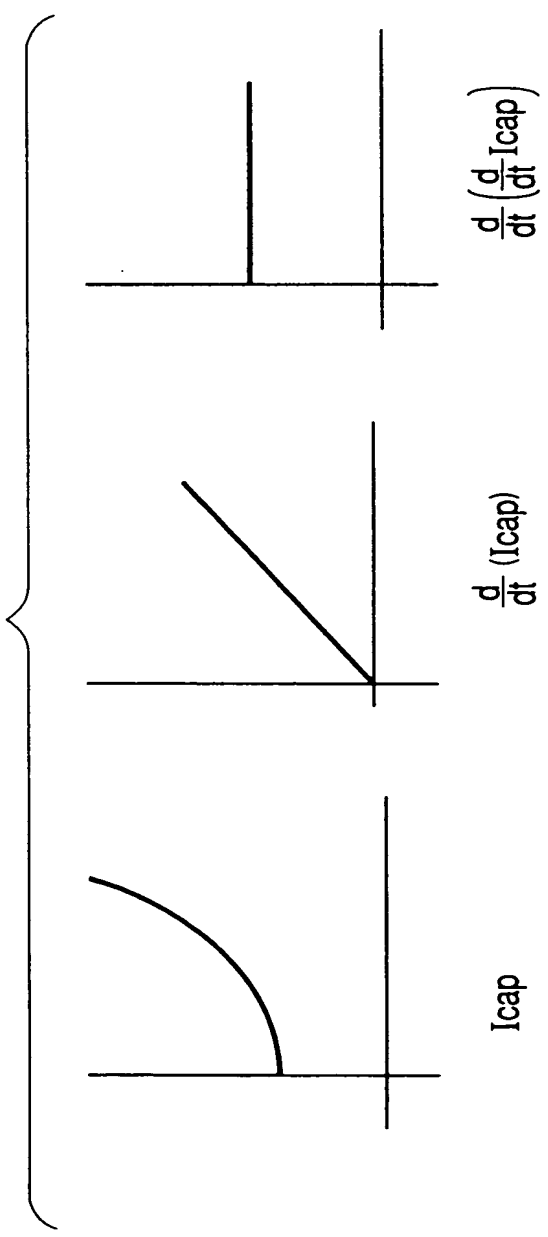




FIG. 9

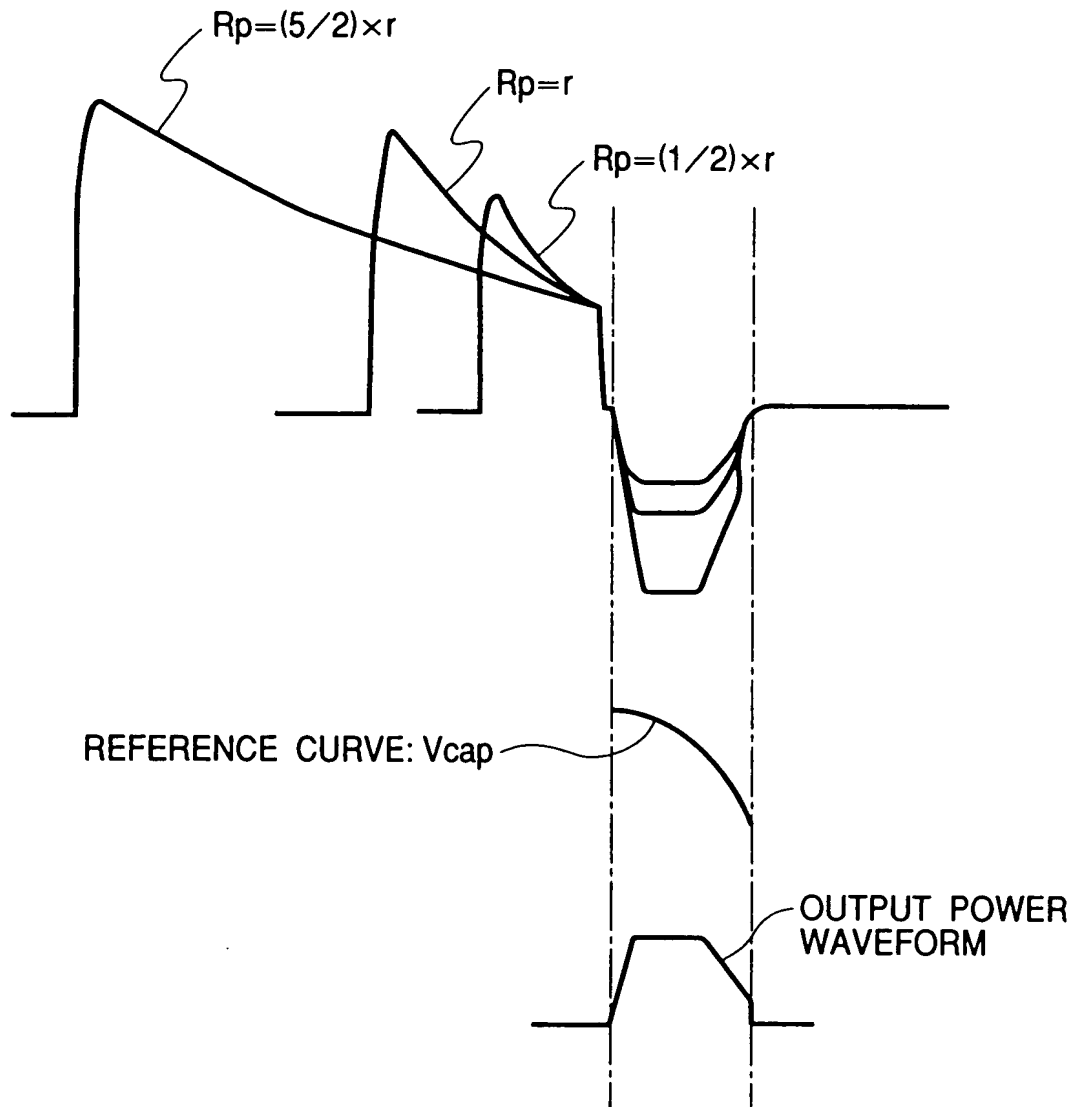




FIG. 10(a)

PRIOR ART

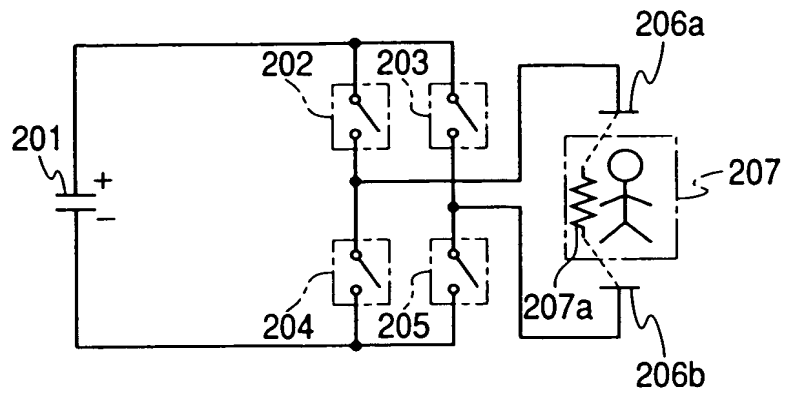


FIG. 10(b)

PRIOR ART

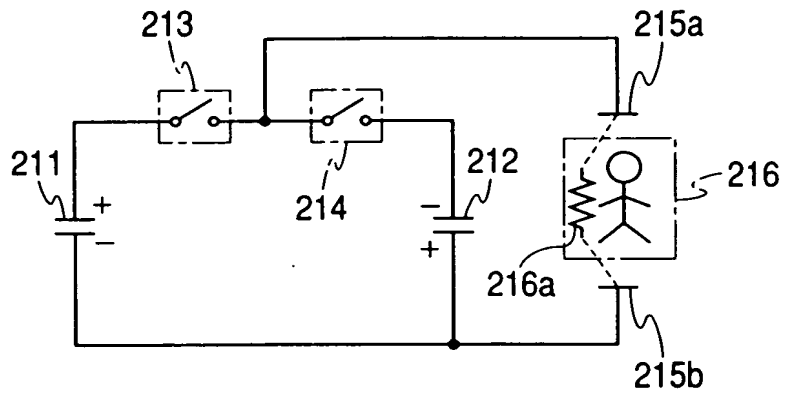


FIG. 11(a)

PRIOR ART

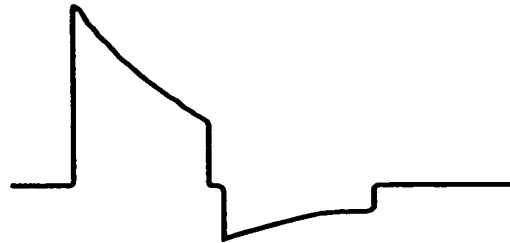


FIG. 11(b)

PRIOR ART

